PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Mahmoud Jibbe
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Art Unit : 2188
Examiner : Doan, Duc T.
Title : A METHOD FOR ESTABLISHING A REDUNDANT ARRAY CONTROLLER MODULE IN A STORAGE ARRAY NETWORK
Attorney Docket No.: LSI.03-0272

MS Appeal Brief-Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

APPELLANT'S PAPER IN RESPONSE TO NOTIFICATION OF NON-COMPLIANT APPEAL BRIEF

This paper responds to Notification of Non-Compliant Appeal Brief mailed on May 25, 2007.

Per M.P.E.P. § 1205.03, when the Patent Office holds an appeal brief to be defective solely due to Appellant's failure to provide a summary of the claimed subject matter, a paper providing a summary of the claimed subject matter as required by 37 CFR 41.37(c)(1)(v) should be submitted.

Please find an amended "SUMMARY OF CLAIMED SUBJECT MATTER" beginning on page 2 of this paper.

SUMMARY OF CLAIMED SUBJECT MATTER

Independent Claim 1 provides:

A storage array network, comprising:

(Specification, paragraphs 6 and 13; FIG. 1, reference characters 20, 30, 72, 74, 82, 84, 90, 100, 102, 106, 112, 116)

a first and second storage array controller module, wherein each storage array controller module has a first and second array controller unit; and

(Specification, paragraphs 6, 12, and 13; FIG. 1, reference characters 72, 74, 82, 84)

an array of storage devices,

(Specification, paragraphs 6 and 13; FIG. 1, reference characters 100, 102, 106, 112, 116)

wherein the first storage array controller module is a primary storage array controller that performs storage array controller functions and the second storage array controller module is a redundant back up,

(Specification, paragraphs 5, 6, and 13; FIG. 1, reference characters 72, 74, 82, 84)

wherein the first storage array controller module provides an availability signal to the second storage array controller module,

(Specification, paragraphs 5, 6, 13, and 14; FIG. 1, reference characters 72, 74, 82, 84)

wherein if the second storage array controller module does not receive a signal from the first storage array controller module within a given period of time, the second storage array controller module asserts control over the array of storage devices.

(Specification, paragraphs 5, 6, 13, and 14; FIG. 1, reference characters 72, 74, 82, 84, 90, 100, 102, 106, 112, 116).

Independent Claim 10 provides:

A method for maintaining operation of a storage array network system, comprising:

(Specification, paragraphs 7, 15, 16, 17, 18, 19, and 20; FIG. 2, reference characters 220, 230, 240, 250, and 260, and FIG. 3, reference characters 305, 310, 315, 320, 325, 330, 335, 340, 345, 350, 355, 360, 365, and 370)

submitting a command to a primary array controller module and a secondary

array controller module;

(Specification, paragraphs 7 and 15; FIG. 2, reference character 220; and FIG. 3, reference character 320)

performing a handshaking protocol between the primary array controller module and the second array controller module to determine which of the primary and the second array controller modules is to process the command;

(Specification, paragraphs 5, 7, 12, 15, 16, 17, 18, 19, and 20; FIG. 2, reference characters 230, 240, 250, and 260, and FIG. 3, reference characters 325, 330, 335, 340, 345, 350, 355, 360, 365, and 370)

removing the command from a queue of the secondary array controller module; and

(Specification, paragraph 16; and FIG. 3, reference character 340)

timing of an aspect of the command.

(Specification, paragraphs 7, 12, 17; FIG. 2, reference character 230, and FIG. 3, reference characters 335, 360, 365, 370, 390, 400, 405)

Independent Claim 22 provides:

A method for establishing a redundant array controller module in a storage array network, comprising:

(Specification, paragraphs 5, 7, and 21; FIG. 4, reference characters 510, 520, 530, 540, 550, 560, 570, and 580)

receiving a command by primary and secondary array controller modules from a host;

(Specification, paragraphs 21 and 22; FIG. 4, reference character 520)

storing the command within two queues, each of the queues being associated with one of the primary and secondary array controller modules;

(Specification, paragraph 21; FIG. 4, reference character 520)

if the primary array controller module processes the command before a time out, then removing the command from the queue of the secondary array controller module; and

(Specification, paragraph 21; FIG. 4, reference character 530)

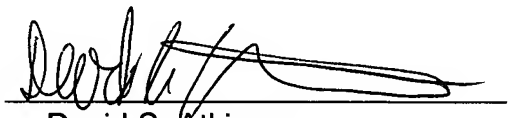
if the primary array controller module times out, then processing the command by the secondary array controller module.
(Specification, paragraph 21; FIG. 4, reference character 540).

Conclusion

For the foregoing reasons, it is respectfully submitted that the Appeal Brief is in compliance with 37 CFR 41.37(c)(1)(v). In each of the rejections discussed under 35 U.S.C. § 103(a), the Examiner has failed to show that the proffered references teach or suggest each and every element of the claimed invention. Accordingly, reversal of all outstanding rejections is earnestly solicited.

Respectfully submitted,
LSI Logic, Inc.,

Dated: June 11, 2007

By: 
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